

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)	
LENOBLE)	Examiner: C. LUU
)	
Serial No. 10/714,440)	Art Unit: 2818
)	
Confirmation No. 9369)	
)	
Filing Date: NOVEMBER 14, 2003)	
)	
For: PROCESS FOR FABRICATING A)	
SHORT-GATE-LENGTH MOS)	
TRANSISTOR AND INTEGRATED)	
CIRCUIT COMPRISING SUCH A)	
TRANSISTOR)	
)	

AMENDMENT

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Responsive to the Official Action of March 27, 2006,
please enter the remarks set out below.